

WE ARE

HIRING



DESIGN ENGINEER

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- Experience** : 5 – 10 years
- Time** : 9 am to 6 pm IST
- Job Type** : Full Time / Contract
- Location** : Hyderabad
- Skills** :
- > Experience in the areas of analog and IO circuit design, With good experience in designing Overvoltage, Failsafe & I2C compatible IO interfaces. Desired Skills Strong hands on experience in CMOS IO design
 - > Solid hands on experience working with Cadence Virtuoso circuit design suite (ADE-XL)
 - > Strong understanding of the impact of device parameter variation on design performance.
 - > Independent ownership of circuit blocks.
 - >

SEND YOUR RESUME

Shiva.m@nbits.tech